

## Announcing the Advisory Board of the IEEE CAS-M

The IEEE Circuits and Systems Magazine (CAS-M) is thrilled to announce the formation of the first Advisory Board, which brings together a distinguished group of academic pioneers and society leaders. The advisory board will provide critical guidance and support to the operation of IEEE CAS-M and help IEEE CAS-M to better serve the members of CAS society. The founding members of IEEE CAS-M Advisory Board are:

Phillip Allen, Professor Emeritus, Georgia Institute of Technology, Atlanta, Georgia (General Chair, ISCAS 1996)

David Allstot, Professor of Electrical & Computer Engineering, Oregon State University, Corvallis, Oregon (CAS Society President, 2008)

Bede Liu, Professor Emeritus, Princeton University, Princeton, New Jersey (CAS Society President, 1982)

Franco Maloberti, Professor Emeritus, University of Pavia, Pavia, Italy (CAS Society President, 2017)

Sanjit K. Mitra, Research Professor in the Department of Electrical & Computer Engineering, University of California, Santa Barbara and Professor Emeritus, Ming Hsieh Department of Electrical Engineering, University of Southern California, Los Angeles, California (CAS Society President, 1986)

George Moschytz, Professor Emeritus, Swiss Federal Institute of Technology (ETH) and Director of the Institute of Signal and Information Theory; Founder and Head of the School of Engineering, Bar-Ilan University, Bar-Ilan, Israel (CAS Society President, 1999)

Ronald A. Rohrer, Cecil & Ida Green Chair and Professor of Electrical & Computer Engineering, Southern Methodist University, Dallas, Texas (CAS Society President, 1987); Founding Editor, IEEE Transaction on Computer Aided Design of Integrated Circuits and Systems (TCAD) 1982–1984

Adel Sedra, Adjunct Professor and Distinguished Professor Emeritus, University of Waterloo, Waterloo, Canada (Associate Editor, IEEE Transactions on Circuits & Systems: 1981–1983 and Editor of CAS Magazine 1985–1986)

Gabor Temes, Professor of Electrical & Computer Engineering, Oregon State University, Corvallis, Oregon (CAS Society Vice President: 1967–1969 and 1971–1972, and Editor of IEEE Transactions on Circuit Theory: 1969–1971)

Alan Willson, Jr., Charles P. Reames Professor Emeritus, Department of Electrical Engineering, University of California, Los Angeles, California (CAS Society President, 1984)

Prof. Sanjit K. Mitra will serve as the founding chair of the advisory board of IEEE CAS-M.

### New Members of IEEE CAS-M's 2021 Editorial Board

We warmly welcome 15 new members to join the IEEE Circuits and Systems Magazine's 2021 Editorial Board. These new members come from different countries and will be in charge of the news and reports from local chapters of IEEE Circuits and Systems Society and special columns.

#### **Mohab Hussein Anis**



Mohab Anis is an Engineering Professor at the American University in Cairo where he teaches electronics & VLSI design, innovation strategy, and economics & management of nanotechnology. Earlier, Mohab was a tenured professor of Electrical & Computer Engineering at University of Waterloo, to which he's now adjunct. He authored 180 international papers, 5 books and 3 US patents. He has been on the Editorial Board of numerous international journals (e.g., IEEE TVLSI, IEEE TCAS-II, ACM-TCAD). Mohab was awarded the Early Research Award from Ontario's Ministry of Research and Innovation, the Colton Medal for Research Excellence. Mohab holds a PhD in Computer Engineering from University of Waterloo (2003), and an MBA from Wilfrid Laurier University with a concentration in Innovation Strategy (2008).

#### **Marco Carli**



Marco Carli is Associate Professor with the Department of Engineering at the Università degli Studi 'Roma TRE', Italy. He received the Laurea degree in Telecommunication Engineering from the Università degli Studi di Roma 'La Sapienza', Italy and the Ph.D. degree from Tampere University of Technology, Finland. He was a Visiting Researcher with the Image Processing Laboratory, UCSB, USA (2000–2004). His research interests are in the area of

digital signal and image processing with applications to multimedia communications. Specifically, he has been working on digital watermarking, multimedia quality evaluation, information security. He is an Associate Editor of EURASIP Journal on Image and Video Processing and Area Editor of Elsevier Signal Processing: Image Communication. He is an IEEE Senior Member.

### **Fan Chen**



Fan Chen is an assistant professor in the Department of Intelligent Systems Engineering at the Indiana University Bloomington. Dr. Chen received her Ph.D. from the Department of Electrical and Computer Engineering at Duke University. Her research interests include computer architecture, emerging nonvolatile memory technologies, and hardware accelerators for emerging applications. Her works have been recognized by the academic community and appeared in top conferences, such as HPCA, DAC, ICCAD, DATE, ASP-DAC, and ESWEK. Dr. Chen won the Best Paper Award and the Ph.D. forum Best Poster Award at ASP-DAC 2018. She is also a recipient of 2019 Cadence Women in Technology Scholarship. Dr. Chen serves as a technical/organizing committee member for several IEEE/ACM conferences, including DAC, ISLPED, GLSVLSI, ISVLSI, ASAP.

### **Anthony C Davies**



My 'roots' are circuit theory fundamentals and active filters and first volunteer activity for the Circuit Theory Group was their first conference outside USA: I was junior co-chair for the 1971 conference in London. I have the privilege to personally know many famous people in the subject. I was co-founder (1993) with the late Wolfgang Schwarz (Dresden) of the annual Non-linear Dynamics of Electronic Systems Workshop (NDES). I was R8 VP of CAS Society (1998 to 2001), active in organizing many CAS Chapter Chair meetings and encouraging new CAS Chapters in Eastern Europe from the 1990s. Being R8 Director and on IEEE BoD gave me good understanding of IEEE organization and now I am mainly involved with History of Technology.

### **Gordana Jovanovic Dolecek**



Gordana Jovanovic Dolecek received PhD degree from the Faculty of Electrical Engineering, University of Sarajevo. In 1995 she joined Institute INAOE, Department for Electronics, Puebla, Mexico, where she works as a full professor. She was a visiting researcher with UCSB, Santa Barbara

in 2001–2002, and 2006, with SDSU San Diego in 2008–2009, and with UCLA, Los Angeles, in 2015–2016. She is author/coauthor of more than 60 journals and 350 conferences papers. Actually she is member of CAS DSP committee and RCM of ISCAS conferences. She is also TPC member of CAS conferences LASCAS. She was the Associate Editor for IEEE TCAS II, and actually she is the Associate Editor for IEEE TCAS I and II and IET Signal Processing.

### **José E. Franca**



José E. Franca, Ph.D. in EE from Imperial College, London, 1985, Doctor of Science honoris causa from the University of Macau, 2006. He is a Professor of the Department of ECE at Instituto Superior Técnico, University of Lisbon, where he founded the Integrated Circuits and Systems Group in 1986. Founder and CEO of Chipidea in 1997, world pioneer in the semiconductor intellectual property industry acquired by Synopsys in 2009. Fellow of the IEEE since 1997, has actively participated in CASS, as member of the Board of Governors, Distinguished Lecturer, Associate Editor, and in the organization of numerous conferences and scientific events worldwide. Dr. Franca is a recipient of the IEEE CASS Golden Jubilee Medal and its Industrial Pioneer Award.

### **Bah-Hwee Gwee**



Bah-Hwee Gwee was the Chairman of IEEE-Singapore Circuits and Systems Chapter in 2005–2006, 2013 and 2016. He was also the Chairman of DSPTC of IEEE Circuits and Systems Society in 2019–2020. He was the General Co-Chair of IEEE DSP 2018 and IEEE SOCC 2019 and is serving the General Co-Chair for IEEE ISICAS 2021. He has also served as Associate Editors for IEEE Transactions of Circuits and Systems II—Brief Express in 2010–2011, 2018–2019 and 2020–2021 and for IEEE Transactions of Circuits and Systems I—Regular Papers in 2012–2013. He was IEEE Distinguished Lecturers for Circuits and Systems Society in 2009–2010 and in 2017–2018. He was one of the keynote speakers for IEEE PAINE 2020 and IEEE APCCAS 2020.

### **Liviu Goras**



Liviu Goras is a professor with the Faculty of Electronics, Telecommunications, and Information Technology, "Gheorghe Asachi" Technical University of Iasi, Romania. He was Chairman of the IEEE Romania Section Circuits and Systems Chapter and Vice president of the IEEE Romania Section. Former senior Fulbright Scholar, with the

Department of Electrical Engineering and Computer Sciences, University of California at Berkeley, his research interests include nonlinear circuit and system theory, neural networks, and signal processing. He is the main organizer of the International Symposium on Signal, Circuits and Systems, ISSCS, held in Iasi every two years since 1993. Dr. Goras was the recipient of the IEEE Third Millennium Medal.

### **André Kaup**



André Kaup is Full Professor and Head of the Chair of Multimedia Communications and Signal Processing at Friedrich-Alexander University Erlangen-Nürnberg, Germany. He served as Head of the Department of Electrical Engineering and was Vice Dean of the Faculty of Engineering. He was a principal engineer and group manager at Siemens Corporate Technology in Munich, Germany. He is a member of the IEEE Multimedia Signal Processing Technical Committee, a Fellow of the IEEE, and a member of the Bavarian Academy of Sciences. He served as Associate Editor for the IEEE Transactions on Circuits and Systems for Video Technology. He has authored around 400 papers and has over 120 patents granted or pending. His research interests include image and video signal processing and coding.

### **Alexander S. Korotkov**



Alexander S. Korotkov received the Dipl. Eng. degree in electronics in 1984, the Ph.D. degree in 1991, and the Dr.Sc. (advanced) degree in electrical engineering in 2000, all from Peter the Great St. Petersburg Polytechnic University, Russia. Dr. Korotkov is currently a professor at the University. He is the author of 3 books and more than 220 articles. His research interests are integrated circuits theory, design, and computer simulations. Dr. Korotkov is the founder and chair of CASS chapter, IEEE, St. Petersburg, the member of the CASCOTech. Comm., IEEE, Secretary of the IEEE Russia North-West Section (2002–2005). Dr. Korotkov has served as an Associate Editor of the IEEE Transactions on Circuits and Systems, Pt II (2003–2005).

### **Zhan Ma**



Prof. Zhan Ma received his Ph.D. from the New York University in 2011, and his B.S. and M.S. from the Huazhong University of Science and Technology, China, in 2004 and 2006 respectively. Dr. Ma is an Associate Professor in the School of Electronic Science and Engineering, Nanjing

University, China. From 2011 to 2014, Zhan worked in several research labs in USA focusing on video coding standardization and codec ASIC design. Dr. Ma's research focuses include computational imaging, and AI-based future video coding. Dr. Ma was awarded the National Science Fund for Excellent Young Scholars, 2018 PCM Best Paper Finalist, 2019 IEEE BTS Best Paper Award, and 2020 IEEE MMSP Grand Challenge Best Image Coding Solution.

### **Ricardo Reis**



Ricardo Reis has an EE Degree from Federal University of Rio Grande do Sul (UFRGS), Brazil, and a Ph.D. degree in Microelectronics from INPG, France. Doctor Honoris Causa by the University of Montpellier (2016). Full professor at UFRGS. His main research includes physical design automation, design methodologies, fault tolerant systems and microelectronics education. More than 650 publications including books, journals and conference proceedings. He received the 2015 IEEE CASS Meritorious Service Award. He was VP of CASS for two terms (2008/2011). Founder of the Rio Grande do Sul CAS Chapter. Founder of several conferences like SBCCI and LASCAS. General/Program Chair of conferences like LASCAS, ICECS, ISVLSI, SBCCI, VLSI-SoC, PATMOS. Member of CASS BoG and CEDA BoG.

### **Branimir Reljin**



Branimir Reljin (SM 2007) received the PhD (1983) degree in Electrical Engineering from the University of Belgrade, Serbia, where he was full professor. His research interests are in CAS and SP in which he (co-)authored more than 500 papers and 4 books. In 2015 he was promoted as Professor Honoris Causa from the University "Tibiscus" in Timisoara, Romania. He is a Member of the Academy of Engineering Sciences of Serbia. He was a Chairman of the joint CAS-SP Chapter of the IEEE Section of Serbia and Montenegro. For past activities this chapter was awarded several times: "Chapter-of-the-Year for Region 8" (2009); "CAS-Society Substantial Activities of the Chapter" (2012); "SP-Society Certificate of Merit" (2015), and "Certificate of Appreciation" (2017).

### **Salil Kumar Sanyal**



Prof. Salil Kumar Sanyal, Ph.D. had been associated with Jadavpur University, Kolkata, India during 1982–2016. His current research domain includes VLSI Circuit Design, Cognitive Radio, Reconfigurable Architecture, CBNS System Design

and Frugal Spectrum Sensing covering 220 nos. of research publications. Dr. Sanyal is a Senior Member of IEEE. He was the Chair of IEEE Kolkata Section during 2006–2008 and also the Chair of CAS Chapter of IEEE Kolkata Section during 2011–2017. He was instrumental in forming the CAS Chapter under IEEE Kolkata Section and was also the key person in promoting Joint Network Meeting at Kolkata in 2015 between the CAS Chapters of IEEE Singapore and Kolkata Sections. He is the advisor of several CAS Chapter Student Branches in Kolkata.

### **M. N. S. Swamy**



M. N. S. Swamy received the Ph.D. degrees in EE from the University of Saskatchewan, Canada. He was conferred in 2009 the title of Honorary Professor at National Chiao Tung University in Taiwan. He is presently a

Research Professor and holds the Concordia Chair in Signal Processing in the Department of Electrical and Computer Engineering at Concordia University, Montreal, Canada, where he served as the Founding Chair of the EE Department from 1970 to 1977, and Dean of Engineering and Computer Science from 1977 to 1993. He has published extensively in the areas of circuits, systems and signal processing. He is the coauthor of nine books and holds five patents. Dr. Swamy is a Fellow of many societies including IEEE. He has served the IEEE in various capacities such as the President in 2004, Vice President during 2001–2002, and in 1976, E.I.C of the IEEE TRANSACTIONS ON CAS-I, Program Chair for the 1973 IEEE CAS Symposium, General Chair for the 1984 IEEE CAS Symposium, He is the recipient of many IEEE-CAS Society awards, including the Education Award, Golden Jubilee Medal, and the Guillemin-Cauer Best Paper Award.

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## **CAS Society Awards 2020**

IEEE CAS is pleased to congratulate the following recipients of the 2020 IEEE Circuits and Systems Society Awards. The Society is especially proud and happy to honor their outstanding accomplishments and we look forward their continuous involvement and close contact with CAS.

### **Mac Van Valkenburg Award**

Joos Vandewalle

### **Charles A. Desoer Technical Achievement Award**

Georges Gielen

### **Industrial Pioneer Award**

Shaojun Wei

### **Guillemin-Cauer Best Paper Award**

Yanling Wei, Jianbin Qiu, and Hamid Reza Karimi

“Reliable Output Feedback Control of Discrete-Time Fuzzy Affine Systems With Actuator Faults,” IEEE Transactions on Circuits and Systems I: Regular Papers, vol. 64, no. 1, pp. 170–181, Jan. 2017.

### **Darlington Best Paper Award**

Francesco Conti, Robert Schilling, Pasquale Davide Schiavone, Antoni Pullini, Davide Rossi, Frank Kağan Gürkaynak, Michael Muehlberghuber, Michael Gautschi, Igor Loi, Germain Haugou, Stefan Mangard, and Luca Benini

“An IoT Endpoint System-on-Chip for Secure and Energy-Efficient Near-Sensor Analytics,” IEEE Transactions on Circuits and Systems I: Regular Papers, vol. 64, no. 9, pp. 248–2494, Sept. 2017.

### **Circuits and Systems for Video Technology Best Paper Award**

Rufael Mekuria, Kees Blom, and Pablo Cesar

“Design, Implementation, and Evaluation of a Point Cloud Codec for Tele-Immersive Video,” IEEE Transactions on Circuits and Systems for Video Technology, vol. 27, no. 4, pp. 828–842, Apr. 2017.

### **Very Large Scale Integration Systems Best Paper Award**

Michael Gautschi, Pasquale Davide Schiavone, Andreas Traber, Igor Loi, Antonio Pullini, Davide Rossi, Eric Flammmand, Frank K. Gürkaynak, and Luca Benini

“Near-Threshold RISC-V Core With DSP Extensions for Scalable IoT Endpoint Devices,” IEEE Transactions on Very Large Scale Integration (VLSI) Systems, vol. 25, no. 10, pp. 2700–2713, Oct. 2017.